

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

EVENT REPORT

Title: INTERNAL HACKATHON for SIH 2026

Venue: Bharathiyar Hall, APJ Block

Date: 31.08.2026 & 01.09.2026

The Department of Electronics and Communication Engineering, in association with the IETE Student Chapter and ECE-IEDC, successfully conducted the “INTERNAL HACKATHON for SIH 2026” over two days at the Bharathiyar Hall, APJ Block, M. Kumarasamy College of Engineering, Karur. The programme aimed to prepare students for the national-level Smart India Hackathon, foster an environment of innovation, and challenge them to build practical solutions under real-world constraints.

The event witnessed enthusiastic participation, with 50 teams competing on the first day and 21 teams on the second day, a total of 71 teams. The sessions were driven by internal faculty expertise and mentorship, with case studies and problem sets on IoT architecture, embedded systems and circuit design. Senior students with prior SIH experience served as evaluators, which proved highly effective and relatable for the participants.

A total of 30 teams were shortlisted to represent the department in the institutional-level scrutiny. The top 15 teams were awarded ₹2,000 per team from a total prize pool of ₹30,000, provided as a project fund to help them procure hardware for their final prototypes.

Participants’ Feedback (61 responses; satisfaction 4.31/5): Participants appreciated the intensive two-day format, mentorship, valuable feedback and the hardware funding, which reduced financial barriers to building robust prototypes. They liked the challenging problem statements that connect classroom theory to real-world applications and the practical insights from senior evaluators.

Areas for Improvement and Recommendations:

- Preparation time: Conduct pre-event technical workshops and pre-reviews before final team selection.
- Time management: Several teams felt rushed or interrupted during pitches. Implement a strict presentation timer and hold Q&A at the end of each pitch.
- Domain grouping: Group future evaluations by domain (IoT, VLSI, etc.) and showcase the funded hardware prototypes in a departmental expo.

Overall, the programme was a grand success. It encouraged participants to recognise their technical potential, and 30 teams were shortlisted on behalf of the ECE department to advance to the intra-college selection phase.

Event Gallery & Prize Distribution

